

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of

HISASHI OHTANI et al

Serial No. 09/197,767

Filed: 11/23/1998

For: SEMICONDUCTOR DEVICE AND

PROCESS FOR PRODUCING THE SAME



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)

) Group Art Unit:

) Examiner:

Eric J. Robinson
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on 3-16-00
Eric J. Robinson

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith attached Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

U.S. Patent Nos. 5,176,789, 5,084,130 and 4,861,622 disclose a flat surface obtained by performing an etching treatment.

U.S. Patent Nos. 5,956,105, 5,838,508, 5,717,224, 5,905,555, 5,990,542, 6,005,648, 5,757,444, 5,963,278, 5,899,547, 5,946,059, 5,990,491 and 5,933,205 disclose a film or a layer having a flat surface.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

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